MULTIMEDIA UNIVERSITY

STUDENT ID NO

MULTIMEDIA UNIVERSITY

FINAL EXAMINATION

TRIMESTER 2, 2018/2019

DET5068 – ANALOG ELECTRONICS 1

(Diploma in Electronic Engineering)

6 MARCH 2019
2:30 PM – 4:30 PM
(2 HOURS)

INSTRUCTIONS TO STUDENT

1. This question paper consists of **6** pages (excluding the cover page and appendices).

2. Answer **ALL** questions. All necessary working steps must be shown.

3. Write all your answers in the answer booklet provided.

QUESTION 1 [25 Marks]

a)

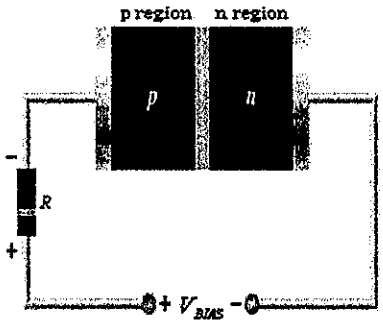

Figure 1

Figure 1 shows the bias condition of a silicon p-n junction diode. Answer the following:

(i) State the bias condition of the p-n junction.

[1 mark]

(ii) Draw the V-I characteristics curve for the above circuit connection.

[3 marks]

(iii) Briefly explain the biasing condition.

[4 marks]

(iv) Determine the behavior of the depletion region in this bias condition.

[1 mark]

b) Briefly explain the formation of n-type and p-type semiconductors. Include any related diagram(s) to support your answer.

[10 marks]

c) Sketch the schematic symbol of forward bias and reverse bias connections. State **ONE** difference between the ideal and practical diode model.

[6 marks]

Continued...

QUESTION 2 [25 Marks]

a) Figure 2(a) and Figure 2(b) below show two types of diode applications respectively.

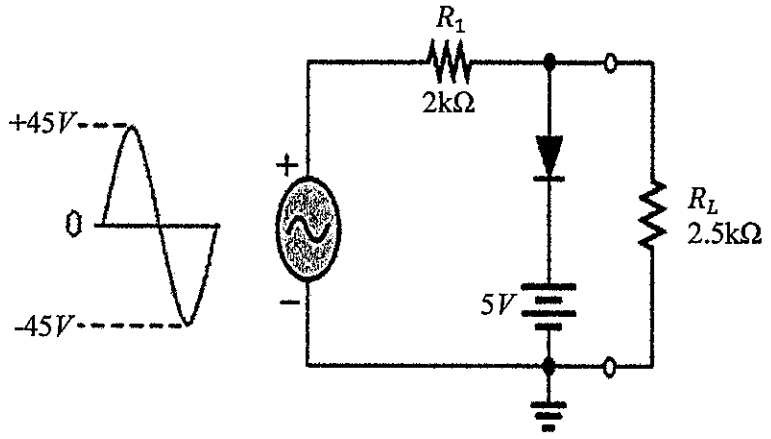

Figure 2(a)

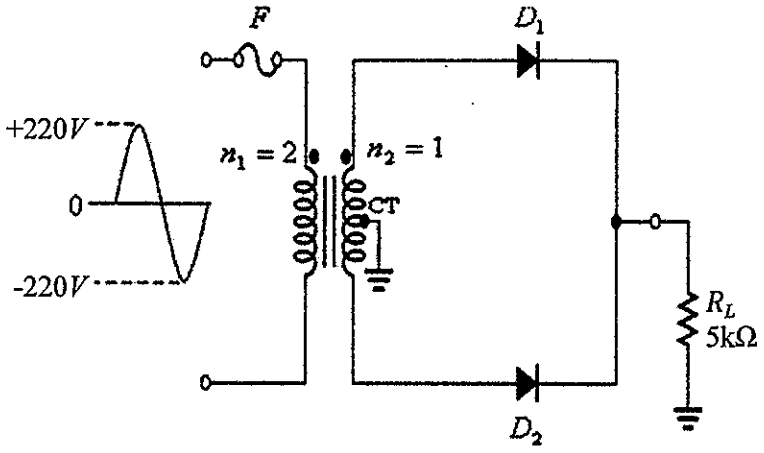

Figure 2(b)

Calculate and sketch the output voltage, $V_{p(out)}$ across R_L for each of the applications above.

[11 marks]

Continued...

b) A Zener diode has the following parameters:

Zener test voltage, $V_{ZT} = 15V$

Zener impedance, $Z_Z = 12\Omega$

Zener test current, $I_{ZT} = 50mA$

Zener knee current, $I_{ZK} = 7mA$

Zener maximum current, $I_{ZM} = 90mA$

Determine the terminal voltage, V_Z when the terminal current, I_Z is given as follows:

(i) $55mA$

[6 marks]

(ii) $100mA$

[2 marks]

(iii) $5mA$

[2 marks]

c) Explain the operation of light-emitting diode (LED). Draw any related diagram(s) to support your answer.

[4 marks]

Continued...

QUESTION 3 [25 Marks]

a)

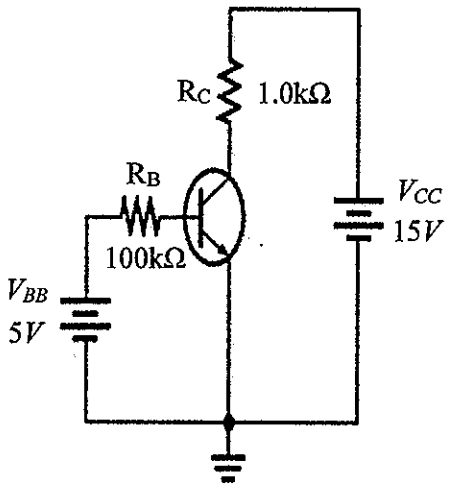

Figure 3(a)

Figure 3(a) shows a certain transistor circuit bias. Determine the value of V_{CE}, V_{BE}, V_{CB}, I_B, I_C, I_E and α_{DC} if β_{DC} = 100. The transistor is made of silicon.

[14 marks]

b)

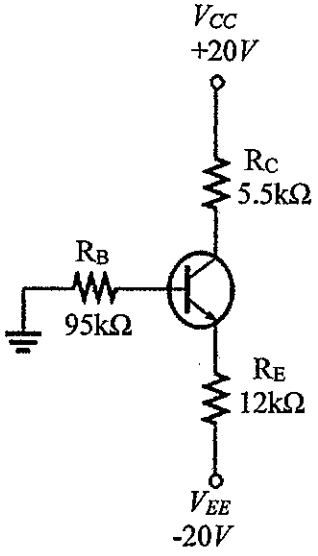

Figure 3(b)

Figure 3(b) shows a certain BJT bias method. Identify the bias method and determine the value of Q-point if β_{DC} = 150. The transistor is made of silicon.

[11 marks]

Continued...

QUESTION 4 [25 Marks]

a)

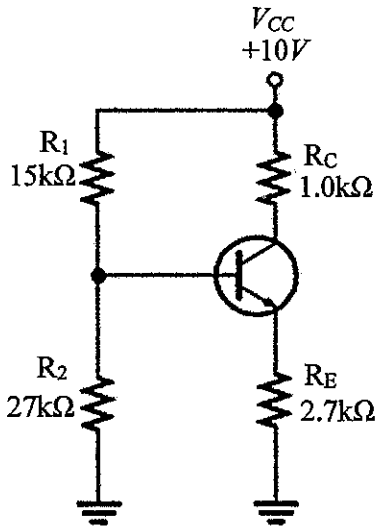

Figure 4(a)

Figure 4(a) shows a certain BJT bias method. If the transistor is made from silicon, identify the bias method and determine the Q-point if $\beta_{DC} = 200$.

[10 marks]

Continued...

b)

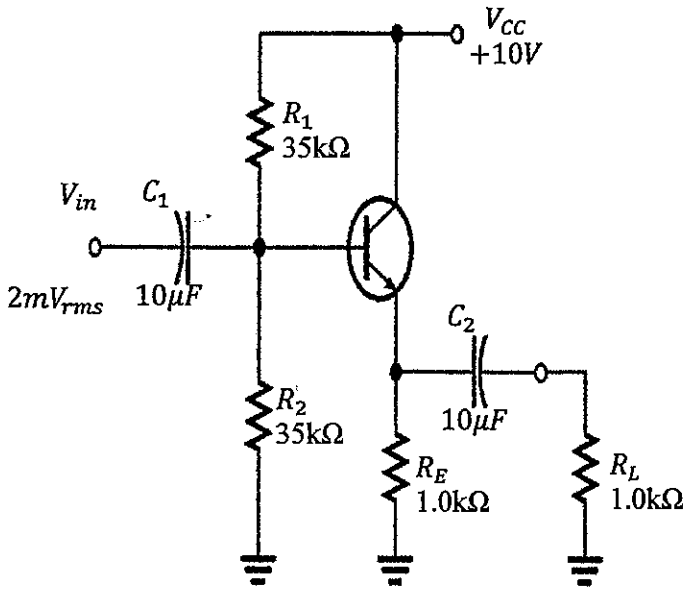

Figure 4(b)

Figure 4(b) shows a certain type of BJT amplifier. Given that $\beta_{ac} = 110$ and $V_{in} = 2mV_{rms}$, answer the following questions:

(i) Identify the type of the BJT amplifier in Figure 4(b).

[1 mark]

(ii) Determine the value of ac total input resistance, $R_{in(tot)}$

[6 marks]

(iii) Determine the value of ac current gain, A_i

[8 marks]

End of Page.

FORMULAE LIST

BJT BIAS	
TERMS	**FORMULAE**
DC load line equation	$I_C = -\frac{1}{R_C}V_{CE} + \frac{V_{CC}}{R_C}$
Voltage divider-bias:	
DC collector and emitter current	$I_C \cong I_E = \frac{V_E}{R_E}$
DC base voltage	$V_B = \frac{R_2 // R_{IN(BASE)}}{R_1 + (R_2 // R_{IN(BASE)})} \times V_{CC}$ $V_B = \frac{R_2}{R_1 + R_2} \times V_{CC}$ if, $R_{IN(BASE)} >> R_2$
DC collector voltage	$V_C = V_{CC} - I_C R_C$
DC collector-to-emitter voltage	$V_{CE} = V_{CC} - I_C(R_C + R_E)$
DC emitter voltage	$V_E = V_B - V_{BE}$
DC input resistance at the base	$R_{IN(BASE)} = \beta_{DC} R_E$
DC total resistance	$R_T = R_2 // \beta_{DC} R_E$
Base bias:	
DC base current	$I_B = \frac{V_{CC} - V_{BE}}{R_B}$

DC collector current	$I_C = \frac{\beta_{DC}(V_{CC} - V_{BE})}{R_B}$
DC collector-to-emitter voltage	$V_{CE} = V_{CC} - I_C R_C$
Collector-feedback bias:	
DC base current	$I_B = \frac{V_C - V_{BE}}{R_B}$
DC collector current	$I_C = \frac{V_{CC} - V_{BE}}{\frac{R_B}{\beta_{DC}} + R_C}$
DC collector-to-emitter voltage	$V_{CE} = V_{CC} - I_C R_C$
Emitter bias:	
DC base current	$I_B \cong \frac{I_E}{\beta_{DC}}$
DC collector current	$I_C \cong \frac{-V_{EE} - V_{BE}}{\frac{R_B}{\beta_{DC}} + R_E}$
DC emitter current	$I_E = \frac{-V_{EE} - V_{BE}}{\frac{R_B}{\beta_{DC}} + R_E}$
DC base voltage	$V_B = V_E + V_{BE}$
DC collector voltage	$V_C = V_{CC} - I_C R_C$
DC collector-to-emitter voltage	$V_{CE} = V_C - V_E$

DC emitter voltage	$V_E = V_{EE} + I_E R_E$
BJT AMPLIFIERS	
TERMS	**FORMULAE**
***r* Parameters:**	
ac base resistance	$r_b^{'} = h_{ie} - \frac{h_{re}(h_{fe}+1)}{h_{oe}}$
ac collector resistance	$r_c^{'} = \frac{h_{re}+1}{h_{oe}}$
ac emitter resistance	$r_e^{'} = \frac{h_{re}}{h_{oe}}$
Common-base amplifier:	
DC emitter current	$I_E = \frac{V_E}{R_E}$
DC base voltage	$V_B = \frac{R_2}{R_1 + R_2} \times V_{CC}$
DC emitter voltage	$V_E = V_B - V_{BE}$
ac output voltage	$V_{out} = V_c$
ac input voltage	$V_{in} = V_e$
ac resistance at emitter	$R_{in(emitter)} \cong r_e^{'}$
ac output resistance	$R_{out} \cong R_C$

Current gain	$A_i \cong 1$
Voltage gain	$A_v \cong \frac{R_c}{r_e'}$
Power gain	$A_p = A_v$
Common-collector amplifier:	
ac input current	$I_{in} = \frac{V_{in}}{R_{in(tot)}}$
ac emitter current	$I_e = \frac{V_e}{R_e}$
ac emitter voltage	$V_e = A_v V_b$
ac input resistance at base	$R_{in(base)} \cong \beta_{ac} R_e$
ac total input resistance	$R_{in(tot)} = R_1 // R_2 // R_{in(base)}$
ac output resistance	$R_{out} \cong \left(\frac{R_s}{\beta_{ac}}\right) // R_E$
Current gain	$A_i = \frac{I_e}{I_{in}}$
Voltage gain	$A_v \cong 1$
Power gain	$A_p = A_i$

Common-emitter amplifier:	
DC collector current	$I_C \cong I_E$
DC base voltage	$V_B = \frac{R_2}{R_1 + R_2} \times V_{CC}$
DC collector voltage	$V_C = V_{CC} - I_C R_C$
DC collector-to-emitter voltage	$V_{CE} = V_C - V_E$
DC emitter current	$I_E = \frac{V_E}{R_E}$
DC emitter voltage	$V_E = V_B - V_{BE}$
DC input resistance at base	$R_{IN(BASE)} = \beta_{DC} R_E$
ac base current	$I_b \cong \frac{I_e}{\beta_{ac}}$
ac base voltage	$V_b = \frac{R_{in(tot)}}{R_s + R_{in(tot)}} \times V_s$ $= I_e r_e'$
ac collector voltage	$V_c = A_v' V_x$
ac total current signal	$I_s = \frac{V_s}{R_{in(tot)} + R_s}$

ac input resistance at base	$R_{in(base)} = \beta_{ac} r'_e$
ac total input resistance	$R_{in(tot)} = R_1 // R_2 // R_{in(base)}$
ac output resistance	$R_{out} \cong R_C$
Current gain	$A_i = \frac{I_c}{I_s}$
Voltage gain	$A_v = \frac{R_C}{r'_e}$ if C_2 exists $A_v = \frac{R_C}{r'_e + R_E}$ if C_2 does not exist
Overall voltage gain	$A'_v = Attenuation \times A_v$
Power gain	$A_p = A'_v A_i$